

David E. Groe

8408 Castleberry Drive
Woodstock, IL 60098

815-479-5177
e-mail: groe@mc.net

Resume on line at <http://user.mc.net/~groe/resume.html>

PROFESSIONAL SUMMARY

Conscientious, thorough, and motivated, results-oriented self starter that is open minded and continuously looking for better, novel, creative, and innovative ways to achieve the optimum solution. Personality fosters long term relationships leading to teamwork. Excellent problem solving, leadership, and communication skills. Seeking a position in the Chicago/Rockford area to best utilize my current skills, abilities, and experience in a management role to contribute to the profitability and success of a firm while learning new aspects of business operations.

PROFESSIONAL EXPERIENCE

TECHNICAL MANAGEMENT

- Direct supervision of eight engineers and technicians
- Electrical design including schematic capture, PCB layout, digital, analog, and FPGA design, components engineering, and system wiring
- Performance reviews, team building, training, and mentoring
- Staffing and hiring including permanent and contract labor
- Preliminary and critical design reviews
- Selection, support, and training of CAD systems and tools for electrical design

PROGRAM MANAGEMENT

- Proposal writing and presenting
- Statements of work
- Budgets, schedules, and risk assessment
- Customer interfacing

PROJECT ENGINEERING

- Overall design responsibility on \$8M program including hardware, software, mechanical, and optical
- Systems engineering
- Specifications and interface control documents
- Preliminary and critical design reviews
- Documentation release, production support, and integration and testing
- Delivery to customer and customer support

DESIGN

- Field programmable gate arrays (FPGAs) for image processing, data formatting, video, and real time applications
- VME and IBM PC compatible platforms
- Embedded microprocessors and controllers
- High speed digital design
- General analog design

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COMPUTER SKILLS

- Business software: Microsoft Word, Excel, PowerPoint, Project 98, Visio
- Internet software: FrontPage Express, HomeSite 4.0, Adobe Photoshop 5.5, Illustrator 9.0, LiveMotion, ImageReady, Internet Explorer, Netscape Communicator
- Programming languages: C++, VHDL, HTML, assembler
- Operating systems: MSDOS, Windows 95/98/2000/NT, UNIX, Mercury Computer's MCOS
- Mentor Graphics: Design Architect, Design entry, Quicksim digital simulation
- Data IO Synario: Design entry, Digital simulation, FPGA tools.
- Orcad: Design entry, Digital simulation, FPGA tools.
- VeriBest: Design View, Design Capture, FPGA Express, VHDL Simulator
- Xilinx tools

WORK EXPERIENCE

Recon/Optical, Inc., Barrington, IL 1994 to present
Manager, Hardware Engineering; Senior Project Engineer; Senior Design Engineer

Sun Electric Corp. / Snap-On Tools, Crystal Lake, IL 1985 to 1994
Design Engineer

Nuvatec Inc., Downers Grove, IL 1983 to 1985
Electrical Engineer

EDUCATION

DePaul University, Kellstadt Graduate School of Business, Chicago
Master of Business Administration (MBA), with Distinction, March 2002
Double concentration in E-Business and Entrepreneurship

Bradley University, Peoria, IL
Bachelor of Science in Electrical Engineering (BSEE), May 1982

CONTINUING EDUCATION

McHenry Community College, McHenry, IL 1992-1994
Computer programming logic, introductory, intermediate, and advanced C programming, object oriented programming (C++), and two accounting classes.

Northern Illinois University, DeKalb, IL 1990
Digital signal processing class

PATENTS

Patent #4,872,004 Plural Source Arbitration System
Patent #4,881,183 Method and Apparatus for Emission Testing